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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Mirmajid Seyyedy

Patent No.: 6,780,675 B2

Issued: August 24, 2004

For: FLIP CHIP TECHNIQUE FOR CHIP

ASSEMBLY

Attorney Docket No.: 2269-2777.3US

CERTIFICATE OF MAILING

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March 2, 200

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Attn.: Certificate of Corrections Branch

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of Correction

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It is noted that several errors appear in this patent of a typographical nature. These errors are due to mistakes in printing on the part of the U.S. Patent and Trademark Office, and occurred through no fault of the Applicant. A certificate of correction in the form attached hereto is requested.

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P.O. Box 2550

Salt Lake City, Utah 84110

Attached hereto in duplicate is Form PTO/SB/44 with at least one copy being suitable for printing.

Respectfully submitted,

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Date: March 2, 2007

JRD/csw

Enclosures: PTO/SB/44 in duplicate

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

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Page 1 of 1

DATED

: August 24, 2004

INVENTOR(S):

Mirmajid Seyyedy

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the claims:

CLAIM 1, COLUMN 6, LINE 43, change "at lest one" to --at least one--

MAILING ADDRESS OF SENDER:

PATENT NO. <u>6,780,675 B2</u>

James R. Duzan 230 South 500 East, Suite 300 Salt Lake City, Utah 84102 USA

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